

**ENGR 16 Course Outline as of Fall 2021****CATALOG INFORMATION**

Dept and Nbr: ENGR 16 Title: ELEC CIRC &amp; DEVICES

Full Title: Electric Circuits and Devices

Last Reviewed: 1/25/2021

Units		Course Hours per Week		Nbr of Weeks	Course Hours Total	
Maximum	4.00	Lecture Scheduled	3.00	17.5	Lecture Scheduled	52.50
Minimum	4.00	Lab Scheduled	3.00	8	Lab Scheduled	52.50
		Contact DHR	0		Contact DHR	0
		Contact Total	6.00		Contact Total	105.00
		Non-contact DHR	0		Non-contact DHR	0

Total Out of Class Hours: 105.00

Total Student Learning Hours: 210.00

Title 5 Category: AA Degree Applicable

Grading: Grade Only

Repeatability: 00 - Two Repeats if Grade was D, F, NC, or NP

Also Listed As:

Formerly:

**Catalog Description:**

Fundamental principles of circuit analysis and an introduction to the theory and use of common electronic devices. Subjects covered include: node and loop analysis, circuit simplification and equivalence, natural and forced response, operational amplifier behavior and circuits, semiconductor theory and behavior, diodes, transistors, and digital circuits. Mathematical concepts reviewed and applied include: matrices and system of equations solutions, binary and hexadecimal numbers, Fourier and Laplace transforms, complex numbers and phasors. Students are required to have a graphing calculator with CAS (Computer Algebra System).

**Prerequisites/Corequisites:**

Course Completion of PHYS 42 AND Course Completion or Current Enrollment in MATH 2

**Recommended Preparation:****Limits on Enrollment:****Schedule of Classes Information:**

Description: Fundamental principles of circuit analysis and an introduction to the theory and use of common electronic devices. Subjects covered include: node and loop analysis, circuit

simplification and equivalence, natural and forced response, operational amplifier behavior and circuits, semiconductor theory and behavior, diodes, transistors, and digital circuits.

Mathematical concepts reviewed and applied include: matrices and system of equations solutions, binary and hexadecimal numbers, Fourier and Laplace transforms, complex numbers and phasors. Students are required to have a graphing calculator with CAS (Computer Algebra System). (Grade Only)

Prerequisites/Corequisites: Course Completion of PHYS 42 AND Course Completion or Current Enrollment in MATH 2

Recommended:

Limits on Enrollment:

Transfer Credit: CSU;UC.

Repeatability: Two Repeats if Grade was D, F, NC, or NP

## **ARTICULATION, MAJOR, and CERTIFICATION INFORMATION:**

<b>AS Degree:</b>	<b>Area</b>	Effective:	Inactive:
<b>CSU GE:</b>	<b>Transfer Area</b>	Effective:	Inactive:
<b>IGETC:</b>	<b>Transfer Area</b>	Effective:	Inactive:
<b>CSU Transfer:</b>	Transferable	Effective:	Fall 1981
		Inactive:	
<b>UC Transfer:</b>	Transferable	Effective:	Fall 1981
		Inactive:	

**CID:**

**Certificate/Major Applicable:**

Major Applicable Course

## **COURSE CONTENT**

### **Student Learning Outcomes:**

At the conclusion of this course, the student should be able to:

1. Apply the complete range of circuit analysis techniques to determine voltages, currents, and power in alternating current, direct current, and transient response circuits.
2. Explain and mathematically characterize the operation of common electronic devices including operational amplifiers, timing and counter chips, digital building blocks, diodes, and transistors.
3. Demonstrate the proper use of electronic lab equipment such as oscilloscopes, multimeters, frequency generators, power supplies, and prototyping boards.
4. Apply professional documentation standards to lab experiment and technical analysis reports.

### **Objectives:**

At the conclusion of this course, the student should be able to:

1. Draw schematic circuit diagrams, labeling and defining voltage and current variables according to standard conventions of component, loop, and nodal analysis.
2. Solve for the voltages, currents, and power in complex direct current circuits with independent and dependent sources using Kirchoff's laws, node and loop analysis, voltage and current dividers.
3. Use parallel and series simplification relationships, Thevenin and Norton equivalence laws, and superposition and suppression principles to simplify circuits.

4. Apply complex numbers, phasor analysis and impedance to find the voltages, currents, and power of complex alternating current circuits with independent and dependent sources.
5. Estimate and compute frequency response, construct and interpret Bode plots, and design, build, and test prototypes of passive and active alternating current filter circuits.
6. Find the transient and total responses caused by step inputs to first- and second-order circuits with given initial conditions.
7. Analyze and test diode circuits using the ideal, offset, graphical and data sheet modeling.
8. Analyze, design and construct bipolar junction transistor circuits using small and large signal models.
9. Construct and predict the output of digital circuits composed of gate, flip-flops, counters and decoders.
10. Model circuits using circuit simulation software.
11. Solder components onto a printed circuit board.
12. Set up and run lab experiments using standard electronic equipment including oscilloscopes, multimeters, frequency counters, signal generators, power supplies, and prototyping boards.
13. Use thorough and careful data collection and analysis techniques and apply industry standard report documentation to deliver professional quality technical reports.

## **Topics and Scope:**

### Lecture & Lab, Topics & Scope

#### I. Principles and Techniques of Direct Current Circuit Analysis

- A. Circuit elements and Kirchhoff's Laws
- B. Voltage and current dividers
- C. Mesh and nodal circuit analysis
- D. Power calculations
- E. Network theorems (Thevenin, Norton, and maximum power)
- F. Graphical solutions for nonlinear circuit elements
- G. Measurement instrumentation (voltmeter, ammeter, and oscilloscope)

#### II. Alternating Current Circuit Analysis

- A. Amplitude (root mean squared and peak), period, phase, and frequency
- B. Sinusoidal voltages and currents
- C. Periodic signals (e.g. square wave, sawtooth)
- D. Review of complex numbers
- E. Phasors
- F. Impedance
- G. Alternating current power
- H. Frequency response and Bode plots
  - I. Natural response
  - J. Total response
- K. Pole-zero diagrams (optional)
- L. Practical applications (e.g. resonant circuits, impedance matching, three-phase circuits)

#### III. Analog Building Blocks

- A. Analog signals and systems
- B. Dependent sources
- C. Modeling concepts
- D. Input and output resistance
- E. Open-circuit voltage amplification
- F. Practical applications (e.g. batteries and other real sources)

#### IV. Semiconductors

- A. Physical and chemical properties of doped semiconductors
- B. Diodes, ideal and non-ideal behavior

- C. Transistor behavior and manufacturing
- D. Transistor biasing and modeling
- E. Graphical circuit analysis for non-linear elements
- F. Practical applications (e.g. amplifiers, logic gates)
- V. Operational Amplifiers
  - A. Characteristics of operational amplifiers
  - B. Circuit analysis assuming ideal op amps
  - C. Non-ideal op amp behavior
  - D. Op amp realizations
  - E. Practical applications (e.g. op amp math circuits)
- VI. Digital Building Block
  - A. Digital signals and binary numbers
  - B. Logic gates (e.g. function and realization)
  - C. Logic chips (e.g. function, realization, and manufacture)
  - D. Digital systems (e.g. combinational logic and memory)
  - E. Practical applications (e.g. state machines, computers)
- VII. Laboratory Exercises
  - A. Resistance measurement and circuit simulation
  - B. Printed circuit board assembly
  - C. Sinusoidal voltage measurements
  - D. Superposition and Thevenin equivalence modeling
  - E. Alternating current analysis and filters
  - F. Operational amplifiers
  - G. Transient response
  - H. Diode rectification
  - I. Transistor biasing and amplification
  - J. Digital counter and display
  - K. Stoplight state machine (optional)

### Assignment:

1. Homework problem sets (10 - 15)
2. Lab worksheets, reports, and technical memos (10 - 15)
3. Quiz(zes) (0 - 10)
4. Examinations (2 - 5)
5. Comprehensive final examination
6. Textbook reading (approximately 40 pages per week)

### Methods of Evaluation/Basis of Grade:

**Writing:** Assessment tools that demonstrate writing skills and/or require students to select, organize and explain ideas in writing.

None, This is a degree applicable course but assessment tools based on writing are not included because problem solving assessments and skill demonstrations are more appropriate for this course.

Writing  
0 - 0%

**Problem Solving:** Assessment tools, other than exams, that demonstrate competence in computational or non-computational problem solving skills.

Homework problem sets and quiz(zes)

Problem solving  
10 - 25%

**Skill Demonstrations:** All skill-based and physical demonstrations used for assessment purposes including skill performance exams.

Lab worksheets, reports, and technical memos, lab exams and quiz(zes)

Skill Demonstrations  
20 - 25%

**Exams:** All forms of formal testing, other than skill performance exams.

Exams and final

Exams  
45 - 60%

**Other:** Includes any assessment tools that do not logically fit into the above categories.

Participation

Other Category  
0 - 10%

### **Representative Textbooks and Materials:**

Principles and Applications of Electrical Engineering. 6th ed. Rizzoni, Giorgio. McGraw-Hill. 2016 (classic)

Electrical Engineering, Principles and Applications. 7th ed. Hambley, Allan. Pearson. 2018  
Instructor-prepared materials