

CATALOG INFORMATION

Dept and Nbr: ELEC 70AL Title: DC AND CONST LAB
Full Title: Direct Current and Electronic Cons Lab
Last Reviewed: 5/5/2008

Units		Course Hours per Week		Nbr of Weeks	Course Hours Total	
Maximum	2.00	Lecture Scheduled	1.00	17.5	Lecture Scheduled	17.50
Minimum	2.00	Lab Scheduled	2.00	17.5	Lab Scheduled	35.00
		Contact DHR	1.00		Contact DHR	17.50
		Contact Total	4.00		Contact Total	70.00
		Non-contact DHR	0		Non-contact DHR	0

Total Out of Class Hours: 35.00

Total Student Learning Hours: 105.00

Title 5 Category: AA Degree Applicable
Grading: Grade Only
Repeatability: 00 - Two Repeats if Grade was D, F, NC, or NP
Also Listed As:
Formerly:

Catalog Description:
Measurement and analysis of DC circuits. Documentation of results in a lab report format. Basic electronic fabrication involving general safety rules, soldering and de-soldering a variety of terminal posts and printed circuit boards, coaxial, and shielded pair cable assembly, and schematic reading.

Prerequisites/Corequisites:
Concurrent Enrollment in ELEC 70A

Recommended Preparation:

Limits on Enrollment:

Schedule of Classes Information:
Description: Analysis and measurement of DC circuits and report writing. (Grade Only)
Prerequisites/Corequisites: Concurrent Enrollment in ELEC 70A
Recommended:
Limits on Enrollment:
Transfer Credit: CSU;

Repeatability: Two Repeats if Grade was D, F, NC, or NP

ARTICULATION, MAJOR, and CERTIFICATION INFORMATION:

AS Degree:	Area		Effective:	Inactive:	
CSU GE:	Transfer Area		Effective:	Inactive:	
IGETC:	Transfer Area		Effective:	Inactive:	
CSU Transfer:	Transferable	Effective:	Fall 1981	Inactive:	Spring 2010
UC Transfer:		Effective:		Inactive:	

CID:

Certificate/Major Applicable:

Certificate Applicable Course

COURSE CONTENT

Outcomes and Objectives:

Upon successful completion of this course the student will be able to:

1. Measure resistance using a VTVM.
2. Connect circuits following a schematic diagram.
3. Measure current through different parts of an Electrical Circuit - using an ammeter.
4. Measure voltage across different points in an Electrical Circuit using a voltmeter.
5. Document lab results using data tables, schematics, and graphs as appropriate.
6. Evaluate lab results through written observations.
7. Pass an Electrical Safety Exam with a grade of 80% or better.
8. Solder and de-solder wires and components to terminals, printed circuit boards, and spring boards by use of hand tools.
9. Tin stranded wires and splice wires by various acceptable techniques.
10. Construct a twisted shielded pair cable and a coaxial cable.
11. Identify and interpret graphic symbols of common electrical components.

Topics and Scope:

- I. Resistance measurement (VTVM)
- II. Circuit connections
 - A. Following a schematic diagram
 - B. Use of a springboard
- III. Electrical Circuit Measurements
 - A. Current - using an ammeter
 - B. Voltage - using a voltmeter
 - C. Meter reading

- IV. Meter connections (In Circuit)
- V. Comparison of theoretical versus measured results
- VI. Electrical safety
 - A. Avoiding electrical shock
 - B. Proper use of test equipment
 - C. General first aid techniques
- VII. Electronic Hand Tools
 - A. Basic concepts
 - 1. proper use
 - 2. maintenance
 - 3. safety
 - B. Hand tools
 - 1. soldering iron
 - 2. microscope
 - 3. de-soldering tools
 - 4. wire cutter
 - 5. pliers
 - 6. routing tools
- VIII. Soldering techniques
 - A. Tinning
 - B. Splicing
 - C. Solder feeding
- IX. Connecting wires and components to terminals and boards
- XI. Coaxial and shielded pair cable assembly
- XII. Identification of electronic symbols on schematic drawings

Assignment:

Lab Reports

- 1. Resistance measurements.
- 2. OHM's Law.
- 3. Series circuit.
- 4. Parallel circuits.
- 5. Series-parallel circuits.
- 6. Internal resistance.
- 7. Maximum power transfer.

Hands-On Test (Skill Demonstration)

Proper use of equipment to verify Kirchhoff's Law.

Construction Projects

- 1. Safety exam.
- 2. Soldering wire terminations.
- 3. Connecting wire and component leads to terminal strips.
- 4. Coaxial and shielded pair cable assembly.
- 5. Printed-Circuit board assembly techniques.
- 6. Reading schematic diagrams.

Reading: approximately 35 pages per week in text.

Methods of Evaluation/Basis of Grade:

Writing: Assessment tools that demonstrate writing skills and/or require students to select, organize and explain ideas in writing.

Lab reports

Writing
30 - 70%

Problem Solving: Assessment tools, other than exams, that demonstrate competence in computational or non-computational problem solving skills.

None

Problem solving
0 - 0%

Skill Demonstrations: All skill-based and physical demonstrations used for assessment purposes including skill performance exams.

Class performances, HANDS ON TEST

Skill Demonstrations
10 - 50%

Exams: All forms of formal testing, other than skill performance exams.

None

Exams
0 - 0%

Other: Includes any assessment tools that do not logically fit into the above categories.

Attendance.

Other Category
5 - 20%

Representative Textbooks and Materials:

Pugh/Ponick. Experiments in Basic Electronics, 5th ed. Glencoe/McGraw Hill, 2003.